

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination DU ET AL.	
		Examiner Andrew O. Arena	Art Unit 2811	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,780,321	07-1998	Shieh et al.	438/34
*	B	US-6,784,463	08-2004	Camras et al.	257/99
*	C	US-5,663,581	09-1997	Holm et al.	257/93
*	D	US-5,612,231	03-1997	Holm et al.	438/23
*	E	US-5,483,085	01-1996	Holm et al.	257/88
*	F	US-5,453,386	09-1995	Holm et al.	438/34
*	G	US-6,423,560	07-2002	Trezza et al.	438/25
*	H	US-5,578,162	11-1996	D'Asaro et al.	438/25
*	I	US-5,385,632	01-1995	Goossen, Keith W.	438/25
*	J	US-6,791,119	09-2004	Slater et al.	257/99
*	K	US-4,213,805	07-1980	Tsukada, Toshihisa	438/40
*	L	US-4,464,211	08-1984	Logan et al.	117/58
*	M	US-3,978,426	08-1976	Logan et al.	372/45.01

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S. and Tauber, R.N. "Silicon Processing for the VLSI Era: Volume 1". Second Edition. 2000, Lattice Press. Pgs 104-107.
	V	Goossen et al. "GaAs MQW Modulators Integrated with Silicon CMOS". IEEE photon. technol. lett. Vol. 7 No. 4, pp 360-362, April 1995.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.